

8. (Amended) A circuit arrangement as claimed in claim 1, characterized in that

a<sup>2</sup> - the npn transistor arrangement (14) is constituted as an npn current mirror and/or particularly as an NMOS current mirror (NMOS = N-channel Metal Oxide Semiconductor = N-type Metal Oxide Semiconductor); and/or

- the pnp transistor arrangement (16) is constituted as a pnp current mirror and/or particularly as a PMOS current mirror (PMOS = P-channel Metal Oxide Semiconductor = P-type Metal Oxide Semiconductor).

9. (Amended) A circuit arrangement as claimed in claim 1, characterized in that the output (18) of the adapter circuit (10) precedes at least a resistor (50) for converting the higher current ( $I_o$ ) output signal into a higher voltage ( $U_o$ ) output signal.

11. (Amended) A circuit arrangement as claimed in claim 1, characterized in that the output (18) of the adapter circuit (10) precedes at least a SCART (= Syndicat des Constructeurs d'Appareils Radio Receteurs et Televiseurs) output (70).

a<sup>3</sup> 12. (Amended) A circuit arrangement as claimed in claim 1, characterized in that the adapter circuit (10) is multi-staged and/or more than one adapter circuit (10) is provided.

a<sup>4</sup> 14. (Amended) A television, multimedia, radio or video recording device comprising at least a circuit arrangement (100) as claimed in claim 1.